

```
01 LBL "SR"      ;Shorten Return Stack - XROM 20,00
02 SIGN
03 SF 10
04 RDN
05 RCL b
06 STO M
07 RDN
08 FC?C 10
09 RTN
10 >"****"      ;Append 3 asterisks
11 RCL IND L
12 ISG L
13 ""           ;F0=null string (NOP)
14 X<> IND L
15 STO N
16 >"***"       ;Append 2 asterisks
17 X<> IND L
18 STO O
19 >"***"       ;Append 2 asterisks
20 X<> O
21 STO a
22 X<> N
23 CLA
24 LBL "Sb"      ;Store b in ROM - XROM 20,01
25 STO b
26 LBL "LR"      ;Lengthen Return Stack - XROM 20,02
27 SIGN
28 RDN
29 "+"          ;1 plus sign
30 RCL a
31 STO N
32 RDN
33 RCL b
34 X<> M
35 STO O
36 ASTO IND L
37 ISG L
38 ""           ;F0=null string (NOP)
39 >"*****"    ;Append 5 asterisks
40 STO O
41 ASTO IND L
42 RDN
43 CLA
44 RTN
45 LBL "UD"      ;Uncover Data Register - XROM 10,08
46 CLA
47 ARCL 00
48 ASTO c
49 SREG 01      ;SigmaREG
50 RTN
51 LBL "HD"      ;Hide Data Registers - XROM 10,20
52 SIGN
53 RDN
54 RCL c
55 "\10"        ;10 hex
56 X<> M
57 >"***"       ;Append 2 asterisks
58 STO N
59 RDN
60 ASTO IND L
61 SREG IND L   ;SigmaREG
62 LBL "~C"      ;~=Sigma - SigmaREG Curtain Exchange - XROM 10,21
63 CLA
64 RCL c
65 STO O
66 STO M
67 >"A"
```

```

68 CLX
69 STO N
70 >"B"
71 STO M
72 >"CD"
73 X<> O
74 X<> d
75 SF 08
76 X<> d
77 X<> N
78 >"E"
79 X<> d
80 SCI IND N
81 X<> d
82 STO O
83 RDN
84 RCL c
85 SREG 00      ;sigmaREG
86 X<> c
87 STO M
88 >"F"
89 RDN
90 RCL N
91 >"GHI"
92 STO L
93 RDN
94 RCL c
95 STO M
96 X<> O
97 >"J"
98 STO M
99 >"K"
100 X<> L
101 STO M
102 >"L"
103 X<> N
104 X<> c
105 RDN
106 CLA
107 END

```

```

00F0C000F30053527AA80A75907C917575AB0A85F47F2A2A2A
90F49674F0CEF49176F37F2A2ACEF49177F37F2A2ACE77917B
CE7687C000F3005362917CC000F3004C527A75F12B907B9176
75907CCE7591779AF49674F0F67F2A2A2A2A2A91779AF47587
85C000F3005544879B009A7D990185C000F30048447A75907D
F110CE75F37F2A2A9176759AF499F4C000F3007E4387907D91
779175F27F41779176F27F429175F37F4344CE77CE7EA808CE
7ECE76F27F45CE7E9DF6CE7E917775907D9900CE7D9175F27F
46759076F47F474849917475907D9175CE77F27F4A9175F27F
4BCE749175F27F4CCE76CE7D7587C0000DB0

```

240 BYTES

HP-41UC SR/LR/UD/HD from PPC ROM PPC V8 N3 P22 May 1981

Program Registers Needed: 35

Row 1 (1 - 6)



Row 2 (6 - 12)



Row 3 (12 - 18)



Row 4 (18 - 24)



Row 5 (24 - 26)



Row 6 (27 - 34)



Row 7 (35 - 39)



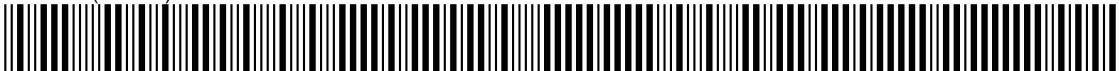
Row 8 (39 - 45)



Row 9 (45 - 51)



Row 10 (51 - 57)



Row 11 (57 - 62)



Row 12 (62 - 69)



Row 13 (69 - 74)



Row 14 (74 - 80)



Row 15 (80 - 87)



Row 16 (87 - 92)



HP-41UC SR/LR/UD/HD from PPC ROM PPC V8 N3 P22 May 1981

Row 17 (92 - 98)



Row 18 (99 - 104)



Row 19 (104 - 107)

